

**AMENDMENT TRANSMITTAL LETTER (Large Entity)**

Applicant(s): Clevenger et al.

Docket No.

YOR920010508US1

Serial No.  
10/039,541Filing Date  
January 2, 2002Examiner  
L. LukGroup Art Unit  
2838Invention: **SYSTEM LEVEL BATTERY INTEGRATION SYSTEM****TO THE ASSISTANT COMMISSIONER FOR PATENTS:**

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

**CLAIMS AS AMENDED**

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	20 -	20 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	6 -	4 =	2 x	\$84.00	\$168.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT</b>					<b>\$168.00</b>

☐ No additional fee is required for amendment.☒ Please charge Deposit Account No. 50-0510  
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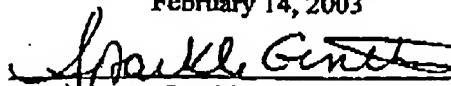
in the amount of \$168.00

☐ A check in the amount of \_\_\_\_\_ to cover the filing fee is enclosed.☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0510  
A duplicate copy of this sheet is enclosed.☒ Any additional filing fees required under 37 C.F.R. 1.16.☒ Any patent application processing fees under 37 CFR 1.17.**FAX RECEIVED**

FEB 14 2003

TECHNOLOGY CENTER 2800

Dated: 2/14/03

  
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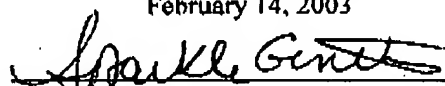
## PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent  
appln. of: Clevenger et al.  
Serial No.: 10/039,541  
Filed: January 2, 2002  
For: SYSTEM LEVEL BATTERY  
INTEGRATION SYSTEM  
Examiner: Luk, Lawrence W.  
Grp. Art Unit: 2838  
Atty. Dkt.: YOR9-2001-0508-US1

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AMENDMENT UNDER 37 C.F.R. §1.111

TECHNOLOGY CENTER 2800

Sir:

In response to the Office Action mailed November 18, 2002, please amend the above-  
identified patent application as follows:

IN THE CLAIMS:

Please substitute the following claims for the same numbered claims in the application.

- 1 1. (Amended) A system level device for battery and integrated circuit chip integration  
2 comprising:  
3 at least one battery;  
4 at least one integrated circuit chip powered by said at least one battery; and

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